



DWS0682NH-Q ESD Protection Diode

Feature

- Bi-directional ESD protection of one line
- 2700Watts peak pulse power ($t_p = 8/20\mu s$)
- Working voltage: 6V
- Junction Capacitance: 570pF(Typ)
- Low clamping voltage
- Low leakage current
- AEC-Q101 qualified
- IEC 61000-4-2 $\pm 30kV$ contact $\pm 30kV$ air
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 150A (8/20 μs)

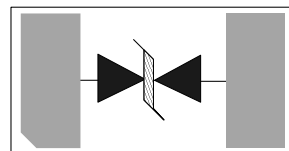
Application

- Microprocessor based equipment
- Personal Digital Assistants (PDA's)
- Notebooks, Desktops, and Servers
- Portable Instrumentation
- Cell Phone Handsets and Accessories

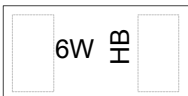
Mechanical Data

- Package: DFN1610-2L
- Molding compound flammability rating: UL 94V-0
- RoHS/WEEE Compliant

Schematic & PIN Configuration



Ordering Information

Part Number	Package	Marking	Packing	Reel Size
DWS0682NH-Q	DFN1610-2L		10000 Tape & Reel	7 inches



Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Units
Peak Pulse Power ($t_p=8/20\mu\text{s}$)	P_{PP}	2700	Watts
Peak Pulse Current ($t_p=8/20\mu\text{s}$) (note1)	I_{pp}	150	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V_{ESD}	± 30 ± 30	kV
Lead Soldering Temperature	T_L	260 (10seconds)	$^{\circ}\text{C}$
Junction Temperature	T_J	-55 to + 125	$^{\circ}\text{C}$
Storage Temperature	T_{stg}	-55 to + 125	$^{\circ}\text{C}$

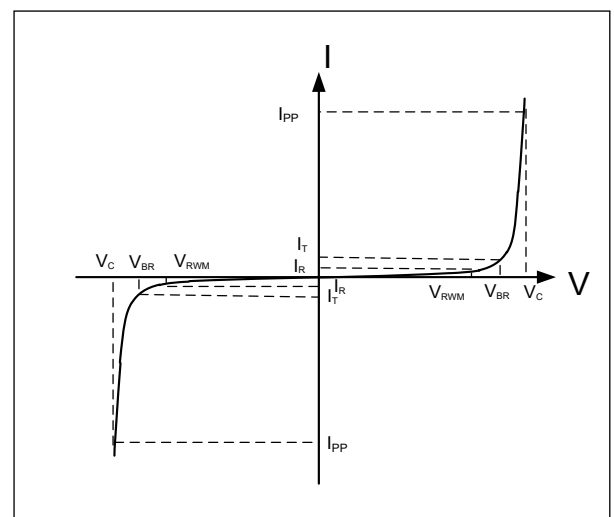
Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	V_{RWM}				6.0	V
Reverse Breakdown Voltage	V_{BR}	$I_T=1\text{mA}$	6.8			V
Reverse Leakage Current	I_R	$V_{RWM}=6.0\text{V}, T=25^{\circ}\text{C}$			1.0	μA
Peak Pulse Current	I_{PP}	$t_p=8/20\mu\text{s}$		150		A
Clamping Voltage	V_C	$I_{PP}=150\text{A}, t_p=8/20\mu\text{s}$		18		V
Junction Capacitance	C_J	$V_R=0\text{V}, f=1\text{MHz}$		570		pF

Electrical Parameters

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current

Note: 8/20 μs pulse waveform.





Typical Characteristics

Figure 1: Peak Pulse Power vs. Pulse Time

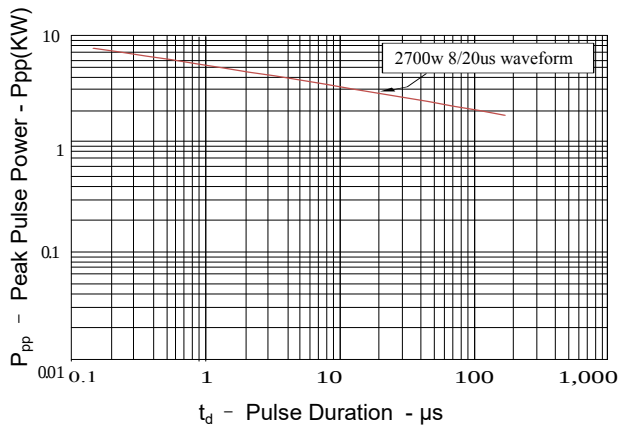


Figure 2: Power Derating Curve

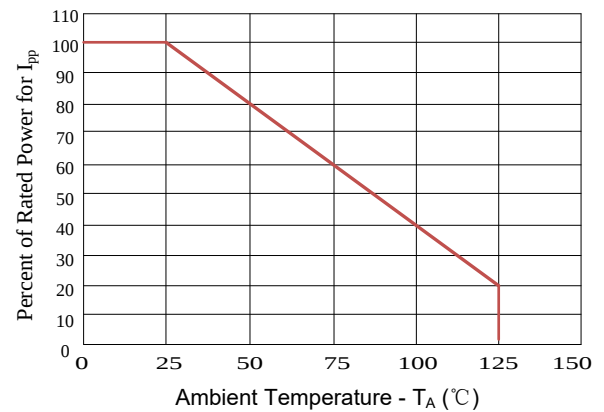


Figure3: Pulse Waveform

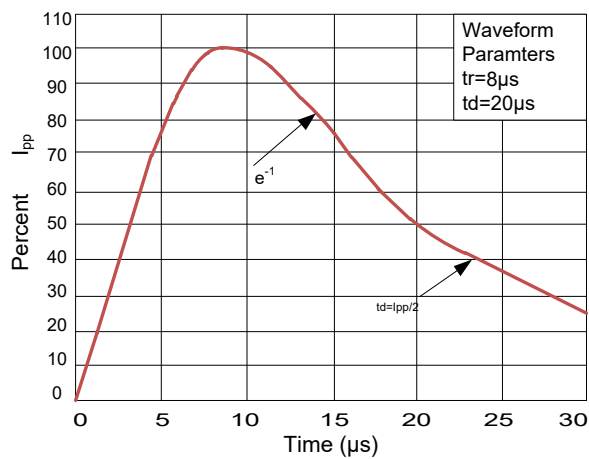
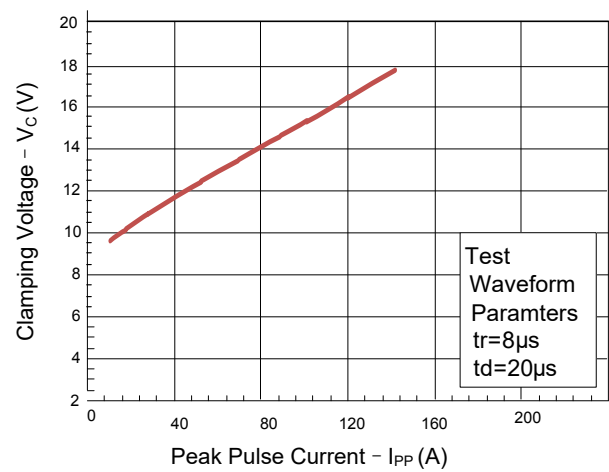
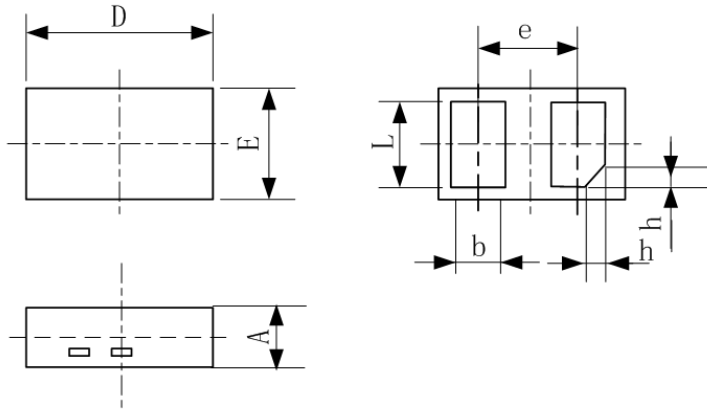


Figure 4: Clamping Voltage vs. Ipp





DFN1610-2L Outline Drawing



COMMON DIEMENSION (MM)			
PKG	DFN1610		
REF.	MIN.	NON.	MAX.
A	0.45	0.50	0.55
D	1.55	1.60	1.65
E	0.95	1.00	1.05
b	0.35	0.40	0.45
L	0.75	0.80	0.85
e	1.10BSC		
h	0.15	0.20	0.25